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Leviatan, Y.; Sheaffer, G.S.;
Microwave Theory and Techniques, IEEE Transactions on ,Volume: 35 , Issue: 1 , Jan 1987
Pages:48 - 59

[Abstract] [PDF Full-Text (1160KB)] **IEEE JNL**

2 **Comments on `Numerical analysis of H-plane waveguide junctions by combination of finite and boundary elements'**
Leviatan, Y.; Sheaffer, G.S.;
Microwave Theory and Techniques, IEEE Transactions on ,Volume: 37 , Issue: 4 , April 1989
Pages:805 - 806

[Abstract] [PDF Full-Text (136KB)] **IEEE JNL**

3 **Composite inductive posts in waveguide-a multifilament analysis**
Sheaffer, G.S.; Leviatan, Y.;
Microwave Theory and Techniques, IEEE Transactions on ,Volume: 36 , Issue: 4 , April 1988
Pages:779 - 783

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4 **The cache injection/cofetch architecture: initial performance evaluation**
Milutinovic, V.; Milenkovic, A.; Sheaffer, G.;
Modeling, Analysis, and Simulation of Computer and Telecommunication Systems, 1997. MASCOTS '97., Proceedings Fifth International Symposium on , 12-15 Jan. 1997
Pages:63 - 64

[Abstract] [PDF Full-Text (196KB)] **IEEE CNF**

5 **Performance and hardware complexity tradeoffs in designing multithreaded architectures**
Bekerman, M.; Mendelson, A.; Sheaffer, G.;
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Masselos, K.; Catthoor, F.; Kakarudas, A.; Goutis, C.E.; De Man, H.;
Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE International Conference on , Volume: 1 , 2-5 Sept. 2001
Pages:285 - 288 vol.1

[Abstract] [PDF Full-Text (456 KB)] **IEEE CNF**

2 **Stochastic approximation and data re-use applied to blind adaptive algorithm for interference cancellation RAKE CDMA receiver**
Zhu, S.; Arasaratnam, P.; Constantinides, A.G.;
Digital Signal Processing, 2002. DSP 2002. 2002 14th International Conference on , Volume: 2 , 1-3 July 2002
Pages:639 - 642 vol.2

[Abstract] [PDF Full-Text (324 KB)] **IEEE CNF**

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Truslove, K.C.E.;
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[Abstract] [PDF Full-Text (536 KB)] **IEE JNL**

4 **Engineering data re-use at JPL: promise and perils**
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Pages:483 - 489 vol.1

[Abstract] [PDF Full-Text (712 KB)] **IEEE CNF**

5 **LSI design toward 2010 and low-power technology**
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[Abstract] [PDF Full-Text (788 KB)] **IEEE CNF**

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Cosatto, E.; Graf, H.P.;
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2 **Design of bit manipulation accelerator for communication DSP**
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3 **The sum-absolute-difference motion estimation accelerator**
Vassiliadis, S.; Hakkennes, E.A.; Wong, J.S.S.M.; Pechanek, G.G.;
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